JUL 2 3 2007 ńnu K. Agarwal, et al.

Title:

**Capacitor Constructions With Enhanced Surface Area** 

Assignee:

Micron Technology, Inc.

Serial No.: 10/050,334

Filed:

January 15, 2002 [RCE filed herewith]

Our Ref.:

MI22-1913

## INFORMATION DISCLOSURE STATEMENT

## PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98

The attached Form PTO-1449 is submitted in compliance with 37 CFR §1.56. Pursuant to Federal Register, Vol. 69, No. 182, pg. 56542 (September 21, 2004), no copies of any cited U.S. patents or U.S. published applications are included herewith. Copies of all other cited art are attached. No admission is made regarding whether the listed references are prior art.

Citation of these references is respectfully requested.

Respectfully submitted,

Dated:

By:

James Reg. No.44,854 Wells St. John P.S. Form PTO-1449

## U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

(Use several sheets if necessary),

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APPLICANT: Vishnu K. Agarwal

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